

## PRODUCT INFORMATION NOTICE LOG

**Part Number Family:** 92-508155-XXX

**Part Description:** BPG8155 14-slot Gen3 Backplane

**Date Established:** 17.Oct.2019

**Last Updated:** 17.October.2019

**By:** Toni Lynn Hauck, Quality Director

Ref #	Reason for the Change	Change Description	Effected Product	Revision (From → To)	Date Effective
794	Initial Release	Release of initial artwork and documentation	All	C01 → D01	17.Jun.2014
814	Product Enhancement	While the passive heatsink is within specification for the two PCI express heatsinks, an active heatsink (with a fan) allows for more margin and should extend product life.	All	D01 → E01	20.Feb.2015
826	Ease of Manufacturing	Improve manufacturability by increasing component spacing.	All	E01 → F02	12.Aug.2015
869 / 870	Product Enhancement	The design incorporated an EEPROM for PCIe GEN3 impedance adjustments, but circuit has not needed. Circuit has been depopulated from the design.  Added pull-up resistors to the I2C bus for unused (no option cards installed) I/O slots.	All	F02 → G02	10.Nov.2016
925 / 926	Product Enhancement	Install PLX recommended workaround for NVIDIA Windows 10 issue.  NVIDIA display adapters may crash the system (blue screen) when display driver is loaded using Windows 10 OS. PLX provided a workaround which requires an updated to the EEPROM for each backplane PCIe switch	All	G02 → H02	14.Dec.2017
3426	Documentation	To clarify drill notes for manufacturability; Changed the drill drawing note for SATA port mechanical mounting holes from non-plated to plated holes. Changed reference to zero tolerance to a non-zero tolerance value.	All	H02 → J03	06.Feb.2018